

Fig. 1
(Prior Art)

100

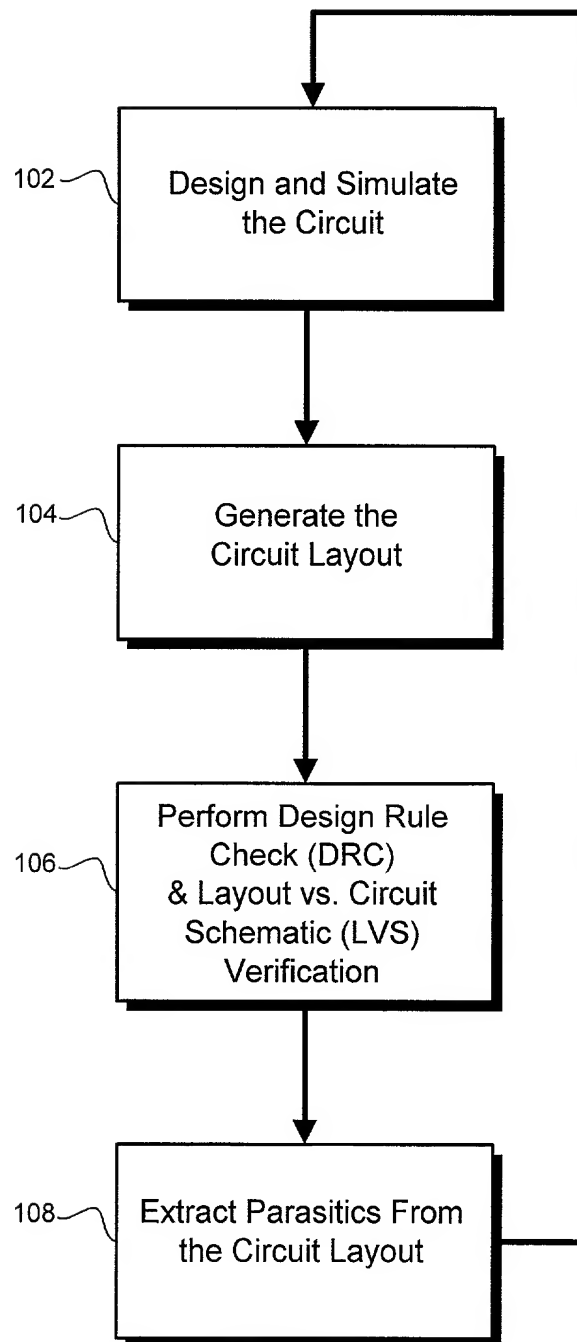


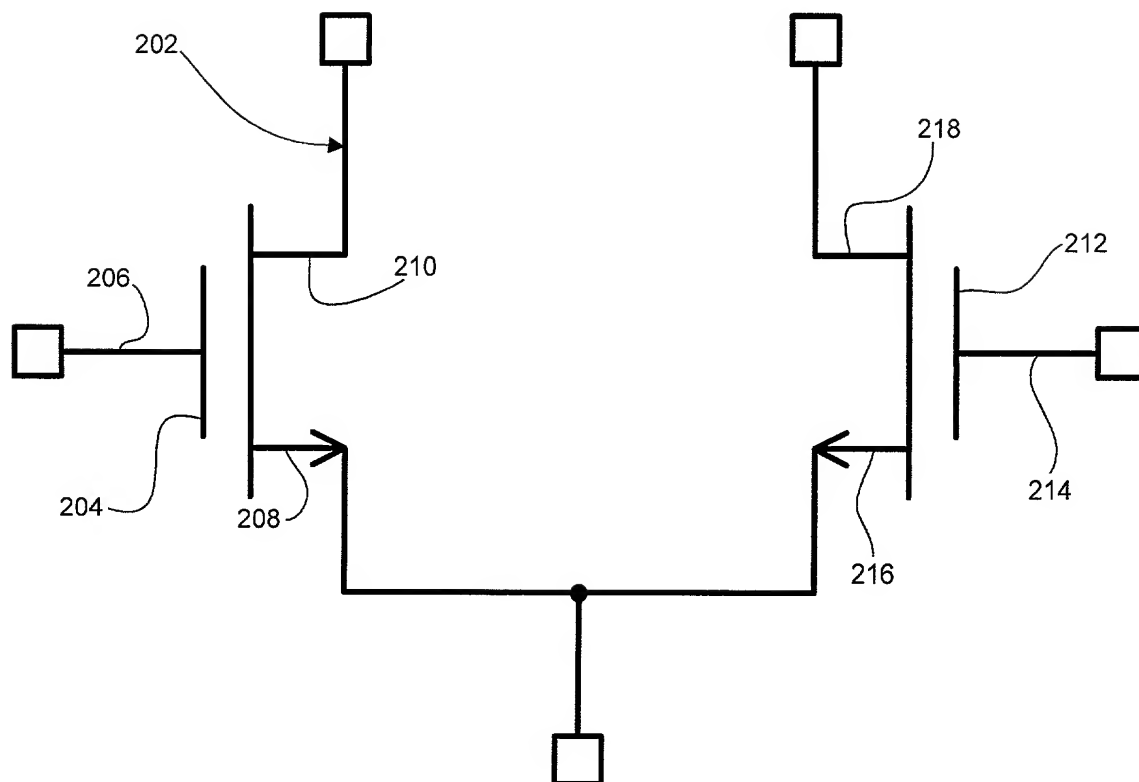
Fig. 2

Differential Pair

Parameters

W_f 220
 L_f 222
 N_f 224
Current 226
Style 228
Slice 230
Bulk Contact 232

Symbol



THE

Parameters

Symbol

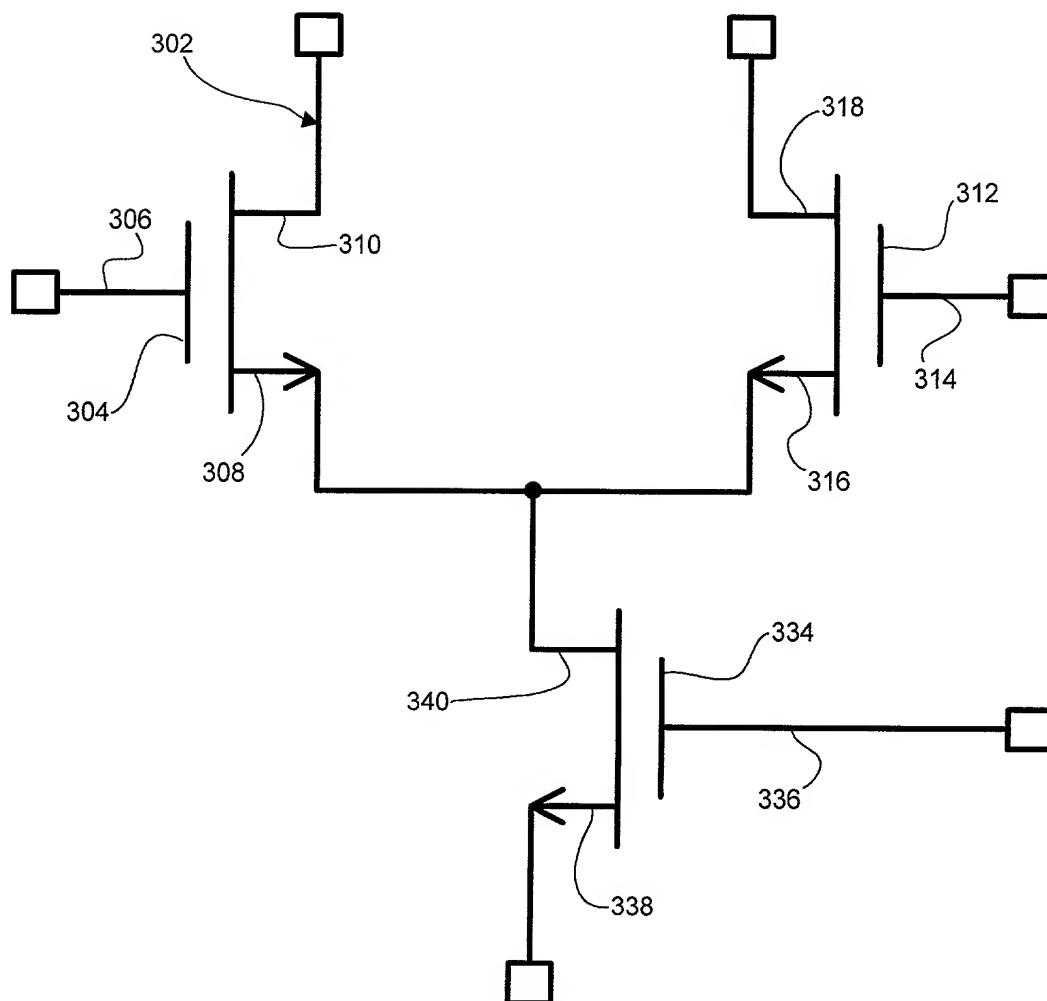


Fig. 4

Current Mirror

Parameters

W_f 420
 L_f 422
 N_f 424
Current 426
Style 428
Slice 430
Bulk Contact 432

Symbol

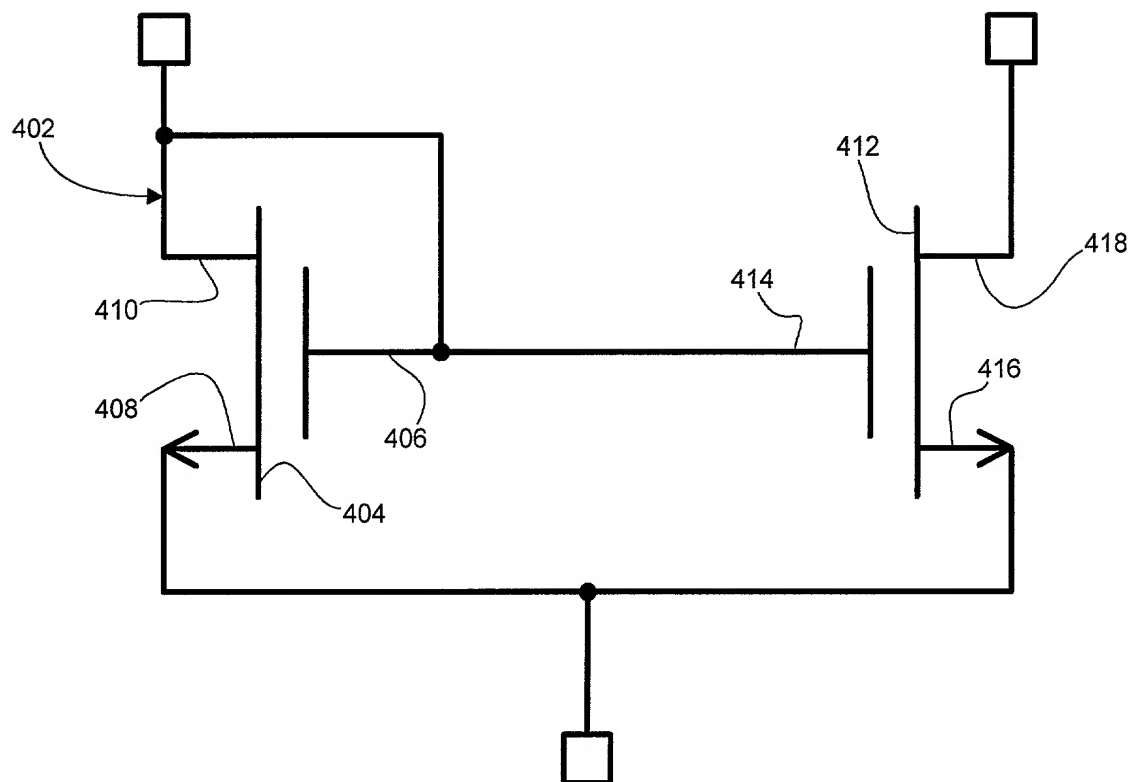


Fig. 5

NMOS Transistor Subcircuit Model

500

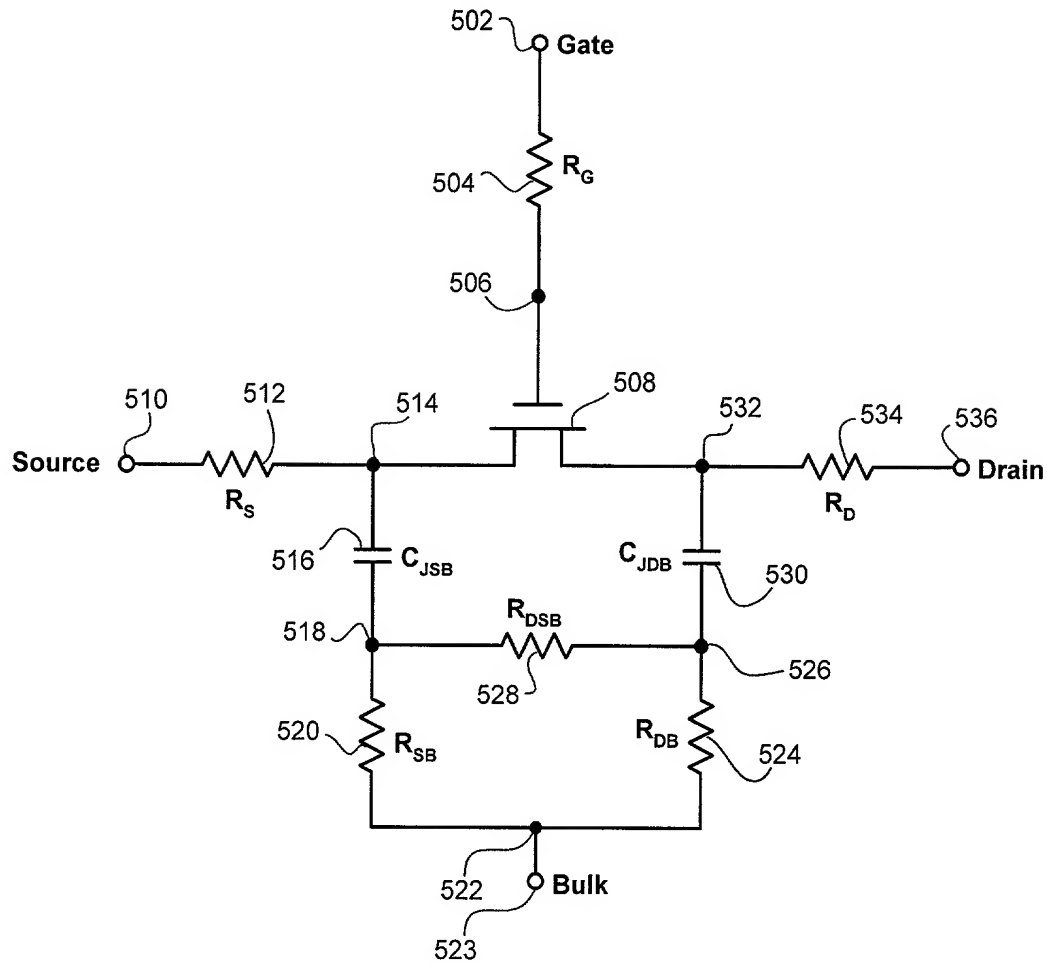


Fig. 7

700

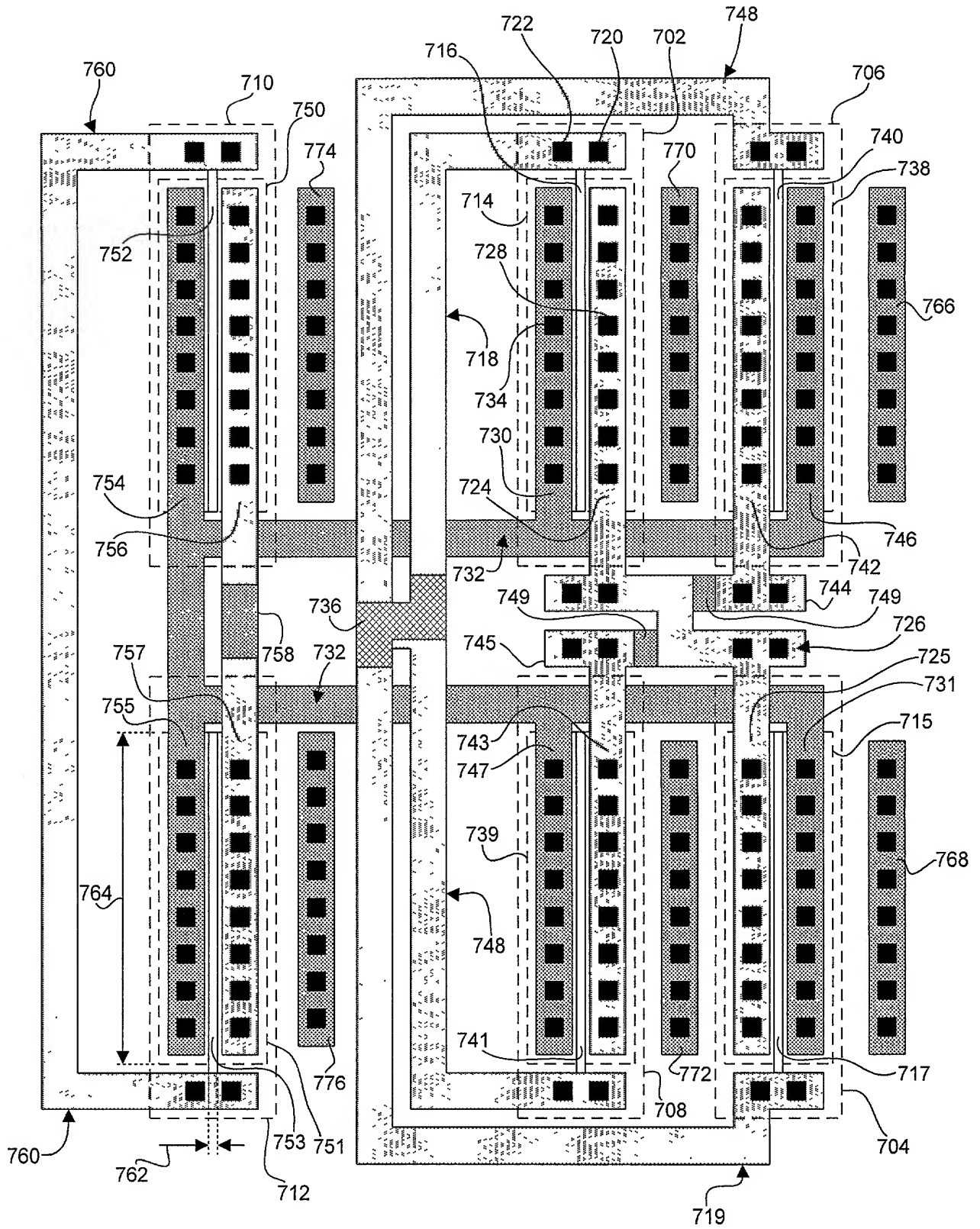


Fig. 8

800

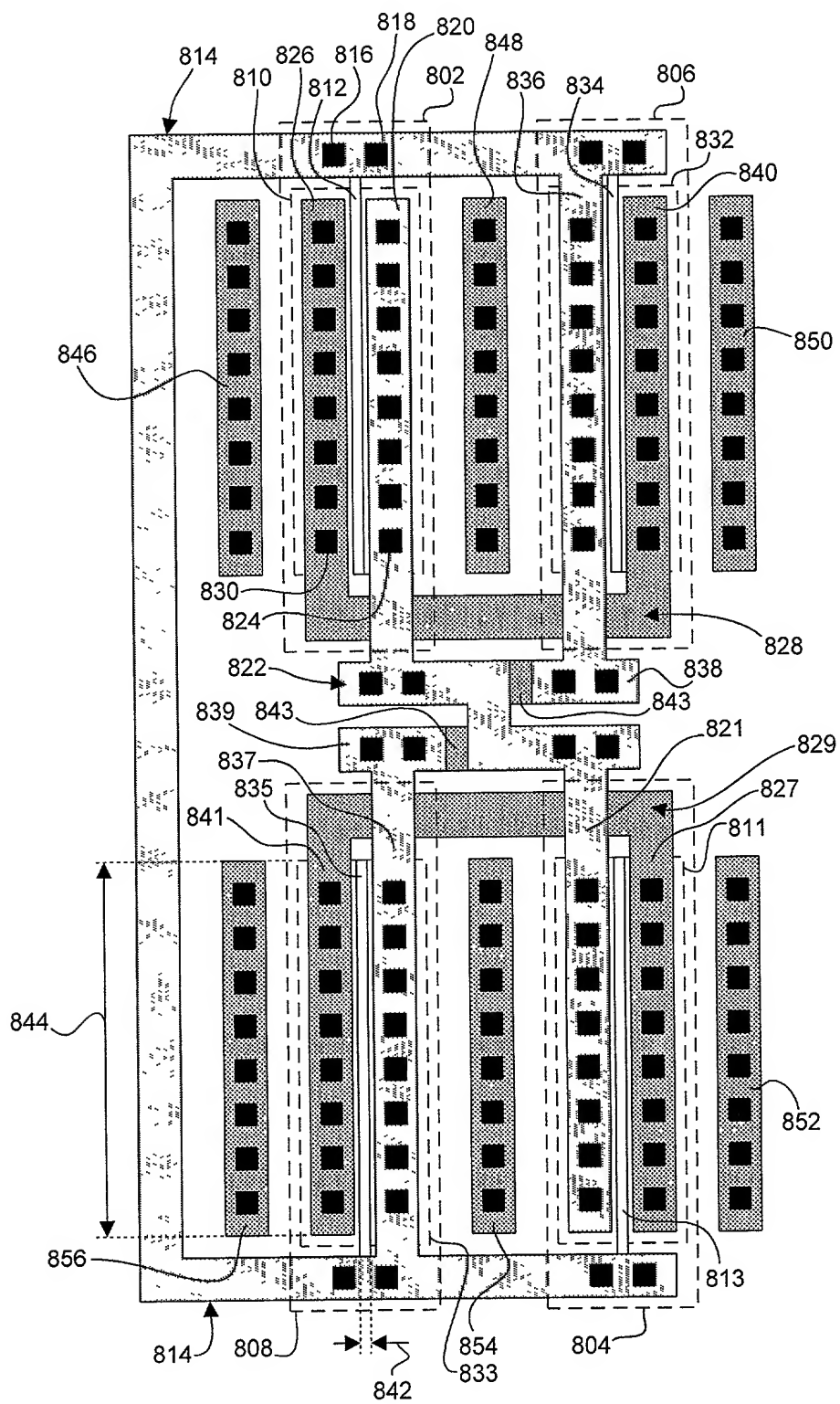


Fig. 9

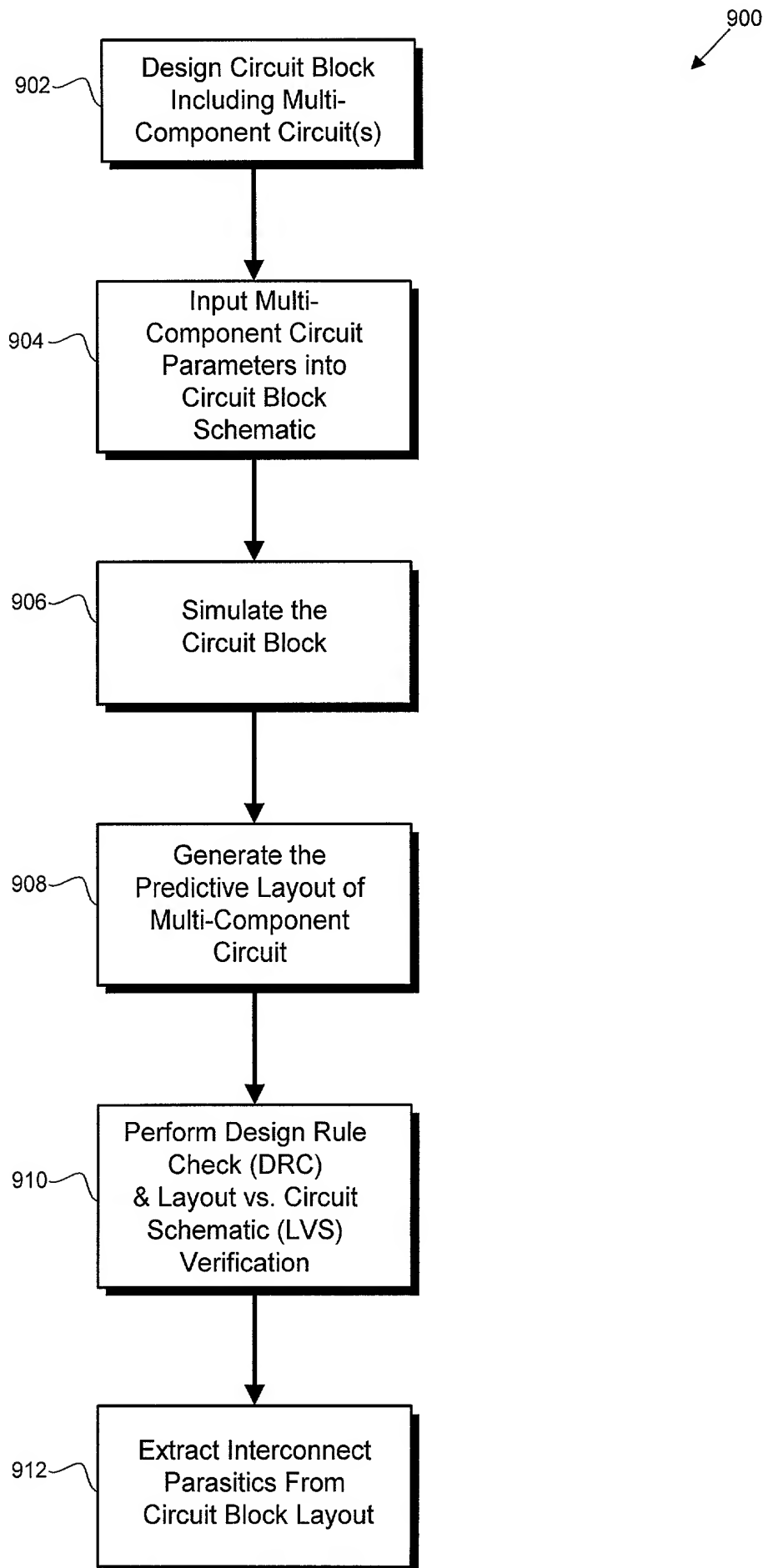


FIG. 10

FIG. 10 is a block diagram of a system architecture.

